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ISSUE CLASSIFICATION	
Class	Subclass

O.I.P.E.	PATENT DATE
TR. SCANNED <u>SB(3)</u> Q.A. <u>U</u>	

APPLICATION NO. 09/769576	CONT/PRIOR F	CLASS 257	SUBCLASS 315	ART UNIT 2811	EXAMINER J. R. W. T. Thoen
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**TITLE** **APPLICANT'S**

Director General

Method for manufacturing split-gate EEPROM memory cell and structure formed thereby

PTO-2040  
F 12/89

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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